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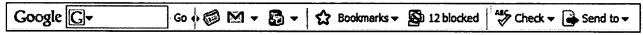
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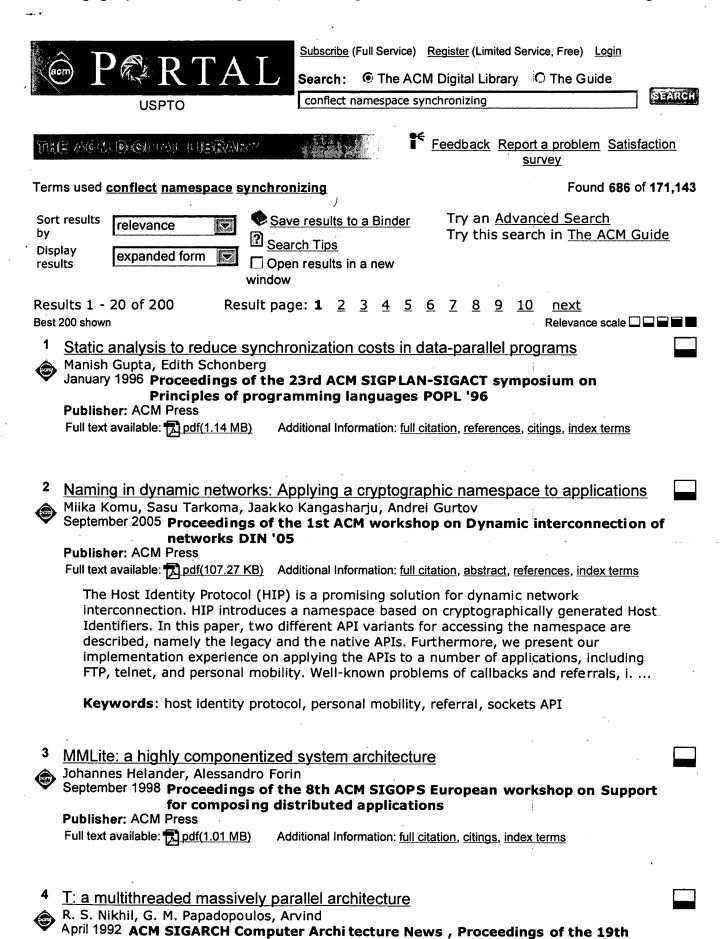| Google | G▾ | | Go ◊ 🕮 M ▾ 🗐 ▾ | ☆ Bookmarks ▾ | 🖻 12 blocked | ABC Check ▾ | 📤 Send to ▾ |

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1 Static analysis to reduce synchronization costs in data-parallel programs   ▬
    Manish Gupta, Edith Schonberg
    January 1996 **Proceedings of the 23rd ACM SIGPLAN-SIGACT symposium on
                 Principles of programming languages POPL '96**
    Publisher: ACM Press
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2 Naming in dynamic networks: Applying a cryptographic namespace to applications   ▬
    Miika Komu, Sasu Tarkoma, Jaakko Kangasharju, Andrei Gurtov
    September 2005 **Proceedings of the 1st ACM workshop on Dynamic interconnection of
                 networks DIN '05**
    Publisher: ACM Press
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    The Host Identity Protocol (HIP) is a promising solution for dynamic network
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    Identifiers. In this paper, two different API variants for accessing the namespace are
    described, namely the legacy and the native APIs. Furthermore, we present our
    implementation experience on applying the APIs to a number of applications, including
    FTP, telnet, and personal mobility. Well-known problems of callbacks and referrals, i. ...

    Keywords: host identity protocol, personal mobility, referral, sockets API

3 MMLite: a highly componentized system architecture   ▬
    Johannes Helander, Alessandro Forin
    September 1998 **Proceedings of the 8th ACM SIGOPS European workshop on Support
                 for composing distributed applications**
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4 T: a multithreaded massively parallel architecture   ▬
    R. S. Nikhil, G. M. Papadopoulos, Arvind
    April 1992 **ACM SIGARCH Computer Architecture News , Proceedings of the 19th**

annual international symposium on Computer architecture ISCA '92, Volume 20 Issue 2
Publisher: ACM Press

Full text available: pdf(1.26 MB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

What should the architecture of each node in a general purpose, massively parallel architecture (MPA) be? We frame the question in concrete terms by describing two fundamental problems that must be solved well in any general purpose MPA. From this, we systematically develop the required logical organization of an MPA node, and present some details of *T (pronounced Start, a concrete architecture designed to these requirements. *T is ...

5 Transactions and synchronization in a distributed operating system

Matthew J. Weinstein, Thomas W. Page, Brian K. Livezey, Gerald J. Popek
December 1985 **ACM SIGOPS Operating Systems Review , Proceedings of the tenth ACM symposium on Operating systems principles SOSP '85**, Volume 19 Issue 5
Publisher: ACM Press

Full text available: pdf(974.32 KB)   Additional Information: <u>full citation</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

6 The KaffeOS Java runtime system

Godmar Back, Wilson C. Hsieh
July 2005 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 27 Issue 4
Publisher: ACM Press

Full text available: pdf(704.30 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

Single-language runtime systems, in the form of Java virtual machines, are widely deployed platforms for executing untrusted mobile code. These runtimes provide some of the features that operating systems provide: interapplication memory protection and basic system services. They do not, however, provide the ability to isolate applications from each other. Neither do they provide the ability to limit the resource consumption of applications. Consequently, the performance of current systems degra ... .

Keywords: Robustness, garbage collection, isolation, language runtimes, resource management, termination, virtual machines

7 Immersion: Utilizing X3D for immersive environments

Johannes Behr, Patrick Dähne, Marcus Roth
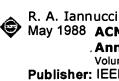April 2004 **Proceedings of the ninth international conference on 3D Web technology**
Publisher: ACM Press

Full text available: pdf(633.61 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

Conceptually, the semantics of X3D describe an abstract functional behaviour of time-based, interactive 3D, multimedia information and do not at all specify a specific software or hardware setup. However, X3D clients and applications today are mainly built for desktop systems running a web-browser.In this paper we explore how suitable X3D and W3C technologies can be utilized as an application and programming model for immersive virtual environments. We present a system implementation, necessary ...

Keywords: computer cluster, human computer interaction, virtual reality

8 Toward a dataflow/von Neumann hybrid architecture

R. A. Iannucci
May 1988 **ACM SIGARCH Computer Architecture News , Proceedings of the 15th
,Annual International Symposium on Computer architecture ISCA '88,**
Volume 16 Issue 2
Publisher: IEEE Computer Society Press, ACM Press

Full text available: pdf(1.29 MB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

Dataflow architectures offer the ability to trade program level parallelism in order to overcome machine level latency. Dataflow further offers a uniform synchronization paradigm, representing one end of a spectrum wherein the unit of scheduling is a single instruction. At the opposite extreme are the von Neumann architectures which schedule on a task, or process, basis. This paper examines the spectrum by proposing a new architecture which is a hybrid of dataflo ...

9 <u>A comparison of the concurrency features of Ada 95 and Java</u>
Benjamin M. Brosgol
November 1998 **ACM SIGAda Ada Letters , Proceedings of the 1998 annual ACM
SIGAda international conference on Ada SIGAda '98,** Volume XVIII Issue 6
Publisher: ACM Press
Full text available: pdf(1.99 MB)   Additional Information: <u>full citation</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

Keywords: Ada, Java, concurrency, inheritance anomaly, object-oriented programming, tasking, threads

10 <u>The J-machine multicomputer: an architectural evaluation</u>
Michael D. Noakes, Deborah A. Wallach, William J. Dally
May 1993 **ACM SIGARCH Computer Architecture News , Proceedings of the 20th
annual international symposium on Computer architecture ISCA '93,** Volume
21 Issue 2
Publisher: ACM Press

Full text available: pdf(1.33 MB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

The MIT J-Machine multicomputer has been constructed to study the role of a set of primitive mechanisms in providing efficient support for parallel computing. Each J-Machine node consists of an integrated multicomputer component, the Message-Driven Processor (MDP), and 1 MByte of DRAM. The MDP provides mechanisms to support efficient communication, synchronization, and naming. A 512 node J-Machine is operational and is due to be expanded to 1024 nodes in March 1993. In this paper we discuss ...

11 <u>File and storage systems: The Google file system</u>
Sanjay Ghemawat, Howard Gobioff, Shun-Tak Leung
October 2003 **Proceedings of the nineteenth ACM symposium on Operating systems
principles**
Publisher: ACM Press
Full text available: pdf(275.54 KB)   Additional Information: <u>full citation</u>, <u>references</u>, <u>index terms</u>

Keywords: clustered storage, data storage, fault tolerance, scalability

12 <u>Human-computer interaction: Schema modelling for automatic generation of
multimedia presentations</u>

Augusto Celentano, Ombretta Gaggi
July 2002 **Proceedings of the 14th international conference on Software engineering and knowledge engineering SEKE '02**
Publisher: ACM Press
Full text available: pdf(169.46 KB) Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>

> Multimedia documents are an effective way to present different kinds of information, since the integration of different media types gives more expressive power and opportunities to catch the user attention. A multimedia report is a multimedia presentation built on a set of data returned by one or more queries to multimedia repositories, integrated according to a schema with appropriate spatial layout and temporal synchronization, and coherently delivered to a user for browsing. We discuss the pr ...

13 <u>The Roma personal metadata service</u>
Edward Swierk, Emre Kiciman, Nathan C. Williams, Takashi Fukushima, Hideki Yoshida, Vince Laviano, Mary Baker
October 2002 **Mobile Networks and Applications**, Volume 7 Issue 5
Publisher: Kluwer Academic Publishers
Full text available: pdf(221.38 KB) Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

> People now have available to them a diversity of digital storage facilities, including laptops, cell phone address books, handheld devices, desktop computers and web-based storage services. Unfortunately, as the number of personal data repositories increases, so does the management problem of ensuring that the most up-to-date version of any document in a user's personal file space is available to him on the storage facility he is currently using. We introduce the Roma personal metadata service t ...

> **Keywords:** data synchronization, distributed data storage, distributed databases, metadata, mobile computing, personal systems

14 <u>A comparison of the object-oriented features of Ada 95 and Java</u>
Benjamin M. Brosgol
November 1997 **Proceedings of the conference on TRI-Ada '97**
Publisher: ACM Press
Full text available: pdf(2.41 MB) Additional Information: <u>full citation</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

15 <u>Improving the browsing experience: WebPod: persistent Web browsing sessions with pocketable storage devices</u>
Shaya Potter, Jason Nieh
May 2005 **Proceedings of the 14th international conference on World Wide Web**
Publisher: ACM Press
Full text available: pdf(166.59 KB) Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

> We present WebPod, a portable system that enables mobile users to use the same persistent, personalized web browsing session on any Internet-enabled device. No matter what computer is being used, WebPod provides a consistent browsing session, maintaining all of a user's plugins, bookmarks, browser web content, open browser windows, and browser configuration options and preferences. This is achieved by leveraging rapid improvements in capacity, cost, and size of portable storage devices. WebPod p ...

> **Keywords:** checkpoint/restart, portable storage, process migration, virtualization, web browsing

16 Performance debugging shared memory parallel programs using run-time dependence analysis

Ramakrishnan Rajamony, Alan L. Cox

June 1997 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1997 ACM SIGMETRICS international conference on Measurement and modeling of computer systems SIGMETRICS '97**, Volume 25 Issue 1

Publisher: ACM Press

Full text available: pdf(2.37 MB)   Additional Information: full citation, abstract, references, citings, index terms

We describe a new approach to performance debugging that focuses on automatically identifying computation transformations to reduce synchronization and communication. By grouping writes together into *equivalence classes*, we are able to tractably collect information from long-running programs. Our performance debugger analyzes this information and suggests computation transformations in terms of the source code. We present the transformations suggested by the debugger on a suite of four ap ...

17 Fine-grain parallelism with minimal hardware support: a compiler-controlled threaded abstract machine

David E. Culler, Anurag Sah, Klaus E. Schauser, Thorsten von Eicken, John Wawrzynek

April 1991 **ACM SIGARCH Computer Architecture News , ACM SIGOPS Operating Systems Review , ACM SIGPLAN Notices , Proceedings of the fourth international conference on Architectural support for programming languages and operating systems ASPLOS-IV**, Volume 19 , 25 , 26 Issue 2 , Special Issue , 4

Publisher: ACM Press

Full text available: pdf(1.41 MB)   Additional Information: full citation, references, citings, index terms

18 Decentralized storage systems: Farsite: federated, available, and reliable storage for an incompletely trusted environment

Atul Adya, William J. Bolosky, Miguel Castro, Gerald Cermak, Ronnie Chaiken, John R. Douceur, Jon Howell, Jacob R. Lorch, Marvin Theimer, Roger P. Wattenhofer

December 2002 **ACM SIGOPS Operating Systems Review**, Volume 36 Issue SI

Publisher: ACM Press

Full text available: pdf(1.87 MB)   Additional Information: full citation, abstract, references

Farsite is a secure, scalable file system that logically functions as a centralized file server but is physically distributed among a set of untrusted computers. Farsite provides file availability and reliability through randomized replicated storage; it ensures the secrecy of file contents with cryptographic techniques; it maintains the integrity of file and directory data with a Byzantine-fault-tolerant protocol; it is designed to be scalable by using a distributed hint mechanism and delegatio ...

19 The M-Machine multicomputer

Marco Fillo, Stephen W. Keckler, William J. Dally, Nicholas P. Carter, Andrew Chang, Yevgeny Gurevich, Whay S. Lee

December 1995 **Proceedings of the 28th annual international symposium on Microarchitecture**

Publisher: IEEE Computer Society Press

Full text available: pdf(1.29 MB)   Additional Information: full citation, references, citings, index terms

20

Granularity of modules in object-based concurrent systems
Peter Wegner
September 1988 **ACM SIGPLAN Notices , Proceedings of the 1988 ACM SIGPLAN workshop on Object-based concurrent programming** , Volume 24 Issue 4
Publisher: ACM Press
Full text available: pdf(363.13 KB)   Additional Information: full citation, abstract, citings, index terms

We examine the interaction of abstraction, distribution, and synchronization in determining the granularity of modules in object-based concurrent systems. The relation between linearizability and serializability as correctness criteria for processes with internal concurrency is explored. Module granularity in object-based programming languages depends on the following factors: the abstraction boundary (unit of encapsulation) the information-hiding interface a module pr ...

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1   Toward a dataflow/von Neumann hybrid architecture
R. A. Iannucci
May 1988 **ACM SIGARCH Computer Architecture News , Proceedings of the 15th Annual International Symposium on Computer architecture ISCA '88,** Volume 16 Issue 2
Publisher: IEEE Computer Society Press, ACM Press

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Dataflow architectures offer the ability to trade program level parallelism in order to overcome machine level latency. Dataflow further offers a uniform synchronization paradigm, representing one end of a spectrum wherein the unit of scheduling is a single instruction. At the opposite extreme are the von Neumann architectures which schedule on a task, or process, basis. This paper examines the spectrum by proposing a new architecture which is a hybrid of dataflo ...

2   Fine-grain parallelism with minimal hardware support: a compiler-controlled threaded abstract machine
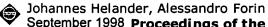David E. Culler, Anurag Sah, Klaus E. Schauser, Thorsten von Eicken, John Wawrzynek
April 1991 **ACM SIGARCH Computer Architecture News , ACM SIGOPS Operating Systems Review , ACM SIGPLAN Notices , Proceedings of the fourth international conference on Architectural support for programming languages and operating systems ASPLOS-IV,** Volume 19 , 25 , 26 Issue 2 , Special Issue , 4
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3   MMLite: a highly componentized system architecture
Johannes Helander, Alessandro Forin
September 1998 **Proceedings of the 8th ACM SIGOPS European workshop on Support for composing distributed applications**
Publisher: ACM Press

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4   Granularity of modules in object-based concurrent systems

Peter Wegner
September 1988 **ACM SIGPLAN Notices , Proceedings of the 1988 ACM SIGPLAN workshop on Object-based concurrent programming** , Volume 24 Issue 4
Publisher: ACM Press
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> We examine the interaction of abstraction, distribution, and synchronization in determining the granularity of modules in object-based concurrent systems. The relation between linearizability and serializability as correctness criteria for processes with internal concurrency is explored. Module granularity in object-based programming languages depends on the following factors: the abstraction boundary (unit of encapsulation) the information-hiding interface a module pr ...

5 Monsoon: an explicit token-store architecture
Gregory M. Papadopoulos, David E. Culler
May 1990 **ACM SIGARCH Computer Architecture News , Proceedings of the 17th annual international symposium on Computer Architecture ISCA '90**, Volume 18 Issue 3a
Publisher: ACM Press

Full text available: pdf(1.87 MB)   Additional Information: full citation, abstract, references, citings, index terms

> Dataflow architectures tolerate long unpredictable communication delays and support generation and coordination of parallel activities directly in hardware, rather than assuming that program mapping will cause these issues to disappear. However, the proposed mechanisms are complex and introduce new mapping complications. This paper presents a greatly simplified approach to dataflow execution, called the explicit token store (ETS) architecture, and its current realization in ...

6 Monsoon: an explicit token-store architecture
Gregory M. Papadopoulos, David E. Culler
August 1998 **25 years of the international symposia on Computer architecture (selected papers)**
Publisher: ACM Press
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7 Improving the browsing experience: WebPod: persistent Web browsing sessions with pocketable storage devices
Shaya Potter, Jason Nieh
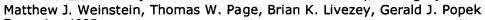May 2005 **Proceedings of the 14th international conference on World Wide Web**
Publisher: ACM Press
Full text available: pdf(166.59 KB)   Additional Information: full citation, abstract, references, index terms

> We present WebPod, a portable system that enables mobile users to use the same persistent, personalized web browsing session on any Internet-enabled device. No matter what computer is being used, WebPod provides a consistent browsing session, maintaining all of a user's plugins, bookmarks, browser web content, open browser windows, and browser configuration options and preferences. This is achieved by leveraging rapid improvements in capacity, cost, and size of portable storage devices. WebPod p ...

> **Keywords:** checkpoint/restart, portable storage, process migration, virtualization, web browsing

8 Transactions and synchronization in a distributed operating system

Matthew J. Weinstein, Thomas W. Page, Brian K. Livezey, Gerald J. Popek
December 1985 **ACM SIGOPS Operating Systems Review , Proceedings of the tenth ACM symposium on Operating systems principles SOSP '85**, Volume 19 Issue 5
Publisher: ACM Press
Full text available: pdf(974.32 KB)   Additional Information: full citation, references, citings, index terms

9 T: a multithreaded massively parallel architecture

R. S. Nikhil, G. M. Papadopoulos, Arvind
April 1992 **ACM SIGARCH Computer Architecture News , Proceedings of the 19th annual international symposium on Computer architecture ISCA '92**, Volume 20 Issue 2
Publisher: ACM Press
Full text available: pdf(1.26 MB)   Additional Information: full citation, abstract, references, citings, index terms

What should the architecture of each node in a general purpose, massively parallel architecture (MPA) be? We frame the question in concrete terms by describing two fundamental problems that must be solved well in any general purpose MPA. From this, we systematically develop the required logical organization of an MPA node, and present some details of *T (pronounced Start, a concrete architecture designed to these requirements. *T is ...

10 Porting RTOS device drivers to embedded Linux

Bill Weinberg
October 2004 **Linux Journal**, Volume 2004 Issue 126
Publisher: Specialized Systems Consultants, Inc.
Full text available: html(22.60 KB)   Additional Information: full citation, abstract

Your old real-time operating system made you do a lot for yourself as a driver author. Take advantage of the facilities Linux offers and clean up some spaghetti code while you're at it.

11 Issues encountered in building a flexible software development environment: lessons from the Arcadia project

R. Kadia
November 1992 **ACM SIGSOFT Software Engineering Notes , Proceedings of the fifth ACM SIGSOFT symposium on Software development environments SDE 5**, Volume 17 Issue 5
Publisher: ACM Press
Full text available: pdf(1.51 MB)   Additional Information: full citation, abstract, references, citings, index terms

This paper presents some of the more significant technical lessons that the Arcadia project has learned about developing effective software development environments. The principal components of the Arcadia-1 architecture are capabilities for process definition and execution, object management, user interface development and management, measurement and evaluation, language processing, and analysis and testing. In simultaneously and cooperatively developing solutions in these areas we learned ...

12 A comparison of Java and C#

Shyamal Suhana Chandra, Kailash Chandra
February 2005 **Journal of Computing Sciences in Colleges**, Volume 20 Issue 3
Publisher: Consortium for Computing Sciences in Colleges

Full text available: pdf(207.37 KB)   Additional Information: full citation, abstract, references, index terms

> Java is becoming very prevalent as a first programming language in some major universities. While other universities are still using C++ and planning to switch to Java, we have a new and emerging language C# (pronounced as C Sharp) on the block. It seems to be the appropriate time to look at these two languages in terms of their introductory programming concepts and discuss the strengths and weaknesses harnessed in each separating one from the other. Keeping this in mind, we have looked at both ...

13 High performance parallel and distributed computation in compositional CC++

Carl Kesselman
April 1996 **ACM SIGAPP Applied Computing Review**, Volume 4 Issue 1
Publisher: ACM Press
Full text available: pdf(270.64 KB)   Additional Information: full citation, abstract, index terms

> C++ was designed to support a wide range of programming styles. A parallel extension to C++ should support parallelism in all the different programming styles that a C++ program might exploit as well as support a broad range of parallel programming paradigms, such as message passing, active objects and shared memory concurrency. Compositional C++ (CC++) is a parallel C++ that was designed with these goals in mind. In this article, we motivate the design of CC++ and provide a brief overview of th ...

14 Breakthrough ideas

James O. Coplien, Brian Foote, Richard P. Gabriel, Dave Thomas, Cristina Lopes, Brian Marick, Bonnie Nardi, Rob Tow, Andrew Hunt, Glenn Vanderburg
October 2005 **Companion to the 20th annual ACM SIGPLAN conference on Object-oriented programming, systems, languages, and applications OOPSLA '05**
Publisher: ACM Press
Full text available: pdf(1.27 MB)   Additional Information: full citation, index terms

> **Keywords**: abstraction, agency, architecture, bayesian inference, communication, culture, design, education, expert systems, fuzzy sets, intention, literary criticism, objects, poetry, postmodernism, programmers, programming, programming language design, reader response criticism, software development

15 Static analysis to reduce synchronization costs in data-parallel programs

Manish Gupta, Edith Schonberg
January 1996 **Proceedings of the 23rd ACM SIGPLAN-SIGACT symposium on Principles of programming languages POPL '96**
Publisher: ACM Press
Full text available: pdf(1.14 MB)   Additional Information: full citation, references, citings, index terms

16 Performance debugging shared memory parallel programs using run-time dependence analysis

Ramakrishnan Rajamony, Alan L. Cox
June 1997 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1997 ACM SIGMETRICS international conference on Measurement and modeling of computer systems SIGMETRICS '97**, Volume 25 Issue 1
Publisher: ACM Press
Full text available: pdf(2.37 MB)   Additional Information: full citation, abstract, references, citings, index terms

We describe a new approach to performance debugging that focuses on automatically identifying computation transformations to reduce synchronization and communication. By grouping writes together into *equivalence classes*, we are able to tractably collect information from long-running programs. Our performance debugger analyzes this information and suggests computation transformations in terms of the source code. We present the transformations suggested by the debugger on a suite of four ap ...

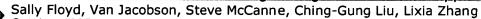**17** Ada-COBOL working group liaison report

Benjamin M. Brosgol
January 1996 **ACM SIGAda Ada Letters**, Volume XVI Issue 1

Publisher: ACM Press

Full text available: pdf(440.34 KB)   Additional Information: full citation, index terms

18 A reliable multicast framework for light-weight sessions and application level framing

Sally Floyd, Van Jacobson, Steve McCanne, Ching-Gung Liu, Lixia Zhang
October 1995 **ACM SIGCOMM Computer Communication Review , Proceedings of the conference on Applications, technologies, architectures, and protocols for computer communication SIGCOMM '95**, Volume 25 Issue 4

Publisher: ACM Press

Full text available: pdf(1.67 MB)   Additional Information: full citation, abstract, references, citings, index terms

This paper describes SRM (Scalable Reliable Multicast), a reliable multicast framework for application level framing and light-weight sessions. The algorithms of this framework are efficient, robust, and scale well to both very large networks and very large sessions. The framework has been prototyped in wb, a distributed whiteboard application, and has been extensively tested on a global scale with sessions ranging from a few to more than 1000 participants. The paper describes the principles tha ...

19 Can dataflow subsume von Neumann computing?

R. S. Nikhil
April 1989 **ACM SIGARCH Computer Architecture News , Proceedings of the 16th annual international symposium on Computer architecture ISCA '89**, Volume 17 Issue 3

Publisher: ACM Press

Full text available: pdf(1.11 MB)   Additional Information: full citation, abstract, references, citings, index terms

We explore the question: "What can a von Neumann processor borrow from dataflow to make it more suitable for a multiprocessor?" Starting with a simple, "RISC-like" instruction set, we show how to change the underlying processor organization to make it multithreaded. Then, we extend it with three instructions that give it a fine-grained, dataflow capability. We call the result P-RISC, for "Parallel RISC." Finally, we discuss memory support for such multipr ...

20 A reliable multicast framework for light-weight sessions and application level framing

Sally Floyd, Van Jacobson, Ching-Gung Liu, Steven McCanne, Lixia Zhang
December 1997 **IEEE/ACM Transactions on Networking (TON)**, Volume 5 Issue 6

Publisher: IEEE Press

Full text available: pdf(310.74 KB)   Additional Information: full citation, references, citings, index terms, review

Keywords: Internetworking, computer network performance, computer networks

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